

Abstracts

Monobit receiver

J.B.Y. Tsui, J.J. Schamus and D.H. Kaneshiro. "Monobit receiver." 1997 MTT-S International Microwave Symposium Digest 2. (1997 Vol. II [MWSYM]): 469-471.

This paper presents a very simple digital receiver design which can cover approximately 1 GHz bandwidth and process two simultaneous signals. The design has the potential to be fabricated on a single chip.

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